

4 Line Chip Array Ferrite Bead 四線陣列鐵氧磁體晶片磁珠

Specifications

Impedance Range : 60 to 1000 Ohms

Operating Temperature Range : -55°C to +125°C

Soldering Method : Reflow or Wave Soldering

Packaging Method : Tape & Reel (per EIA Specifications)

Storage Temperature : -40°C to +85°C, 70% RH Max

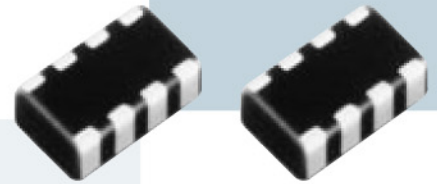
阻抗值範圍：60 至 1000 Ohms

操作溫度範圍：-55°C 至 +125°C

焊法：迴焊或波焊

包裝：捲帶（依EIA規格）

儲存溫度：-40°C 至 +85°C，相對溼度上限：70%



Features 特色

Combines 4 single 1608 (0603) chips into one package 3216 (1206) to reduce board space, and placement time. The 0.8mm terminal pitch makes easy to apply EMI prevention in multiple-lines such as connectors and IC pins. Material and construction design minimize cross talk between adjacent circuits.

整合四顆1608(0603)元件而成為單一的3216(1206)磁珠，減少置放於電路板時所需的時間與空間。0.8mm 端子間距，使得在如抑制連接器及積體電路等多重線路接點間電磁干擾的應用上更加便利。特殊的材料和結構設計，可使相鄰線路間的干擾降至最低。

Application Field 應用領域

Filtering between analog and digital circuitry, clock generation circuitry, I/O interconnects, isolation between RF noisy circuits and logic devices susceptible to functional degradation, power supply filtering to prevent conducted RF energy from corrupting the power generation circuitry, high frequency EMI prevention of computers, printers, VCRs, TVs and portable telephones.

類比與數位線路之間的濾波、週期性訊號產生線路、輸入/輸出連結埠、隔絕微波雜訊線路與易功能性衰減的邏輯運算元件、過濾微波雜訊以確保電源供應器所提供之能量品質、電腦 / 印表機 / 攝錄影機 / 電視以及行動電話等的高頻電磁干擾濾波器。

| Series | Size | | | Impedance | Test Freq. | DCR max. | Rated Current | |
|--------|----------------------|---------------------|-------------------|-------------|------------|------------|---------------|--------|
| 產品系列 | 尺寸 | | | 阻抗(Ω) | 測試頻率(MHz) | 直流電阻最大值(Ω) | 額定電流(mA) | |
| CA1206 | 3.2(L)* 0.126(l)* | 1.6(W)* 0.06(W)* | 0.8(T) 0.03(T) | mm/ inch | 60-1000 | 100 | 0.12-0.8 | 300-50 |

4 Line Chip Array Ferrite Bead For High Speed 四線陣列鐵氧磁體高速晶片磁珠

Specifications

Impedance Range : 120 to 300 Ohms

Operating Temperature Range : -55°C to +125°C

Soldering Method : Reflow or Wave Soldering

Packaging Method : Tape & Reel (per EIA Specifications)

Storage Temperature : -40°C to +85°C, 70% RH Max

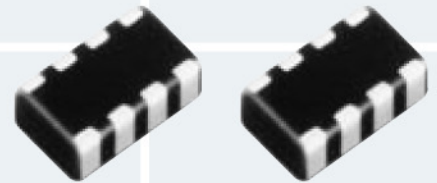
阻抗值範圍：120 至 300 Ohms

操作溫度範圍：-55°C 至 +125°C

焊法：迴焊或波焊

包裝：捲帶（依EIA規格）

儲存溫度：-40°C 至 +85°C，相對溼度上限：70%



Features 特色

Combines 4 single 1608(0603) chips into one package 3216(1206) to reduce board space, and placement time. The 0.8mm terminal pitch makes easy to apply EMI prevention in multiple-lines such as connectors and IC pins. Material and construction design minimize cross talk between adjacent circuits.

整合四顆1608(0603)元件而成為單一的3216(1206)磁珠，以減少置放於電路板時所需的時間與空間。0.8mm的端子間距使得在如抑制連接器及積體電路等多重線路接點間電磁干擾的應用上更加便利，而經設計的材料和結構可使相鄰線路間的干擾降至最低。

Application Field 應用領域

Filtering between analog and digital circuitry, clock generation circuitry, I/O interconnects, isolation between RF noisy circuits and logic devices susceptible to functional degradation, power supply filtering to prevent conducted RF energy from corrupting the power generation circuitry. Sharp impedance characteristics can effectively minimize attenuation, high frequency EMI prevention of LCD monitor, PDA, computers, computer peripherals, cellular equipment, digital TV, digital cameras, audio/visual equipment, DVD, wireless communication devices, MP3.

類比與數位線路間的濾波、週期性訊號線路、輸入/輸出連接、隔絕微波雜訊線路與易功能性衰減的邏輯運算元件、過濾微波雜訊以確保電源供應器所提供之能量品質。陡峭的阻抗特性能有效減少液晶螢幕、PDA、電腦、電腦週邊設備、數位電視、數位相機、視聽設備、DVD、MP3等之訊號衰減及高頻電磁波干擾之抑制。

| Series | Size | | | Impedance | Test Freq. | DCR max. | Rated Current | |
|--------|----------------------|---------------------|-------------------|-------------|------------|------------|---------------|-----|
| 產品系列 | 尺寸 | | | 阻抗(Ω) | 測試頻率(MHz) | 直流電阻最大值(Ω) | 額定電流(mA) | |
| CD1206 | 3.2(L)* 0.126(l)* | 1.6(W)* 0.06(W)* | 0.8(T) 0.03(T) | mm/ inch | 120-300 | 100 | 0.3-0.4 | 150 |